

FIG. 2

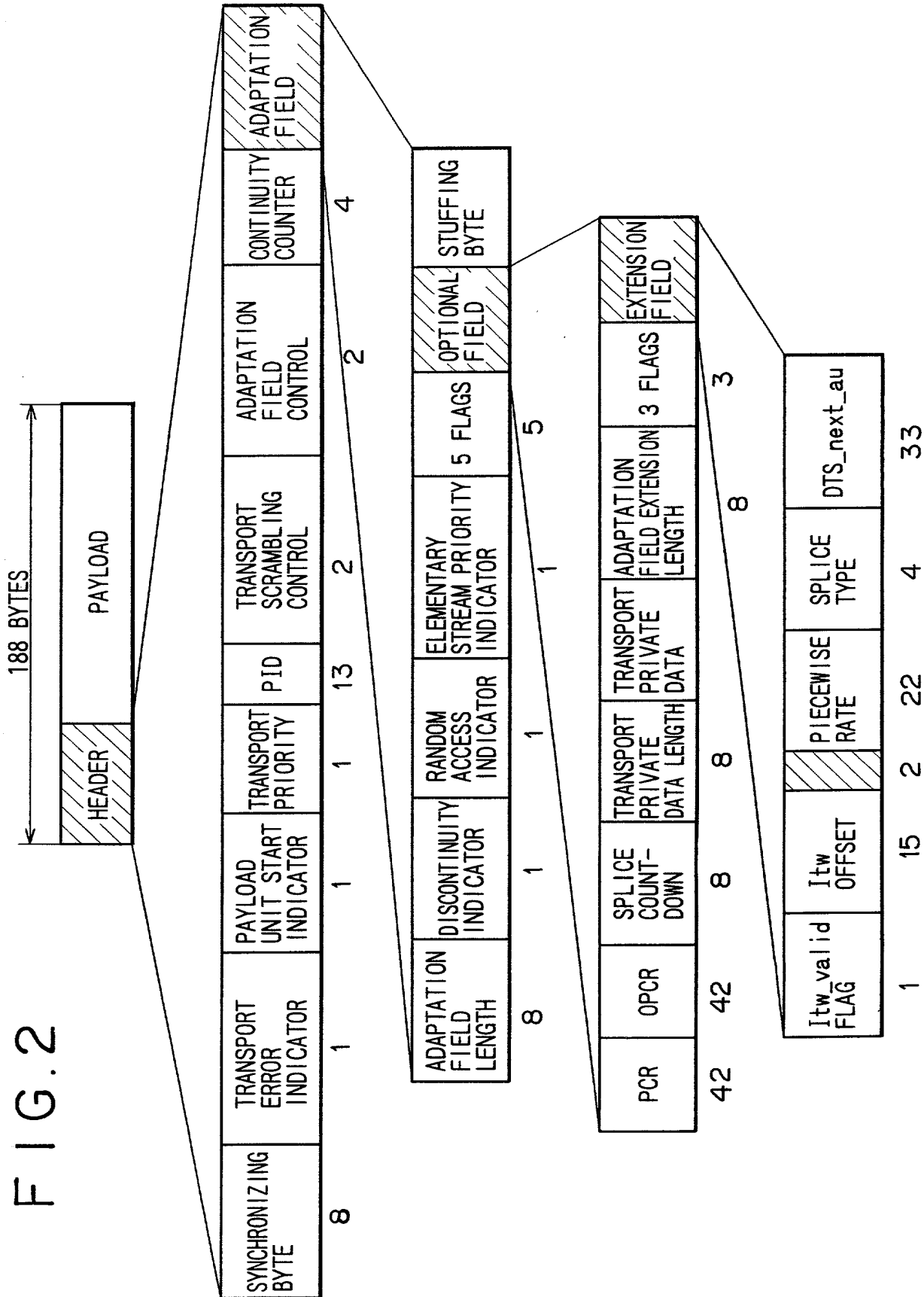


FIG. 3A

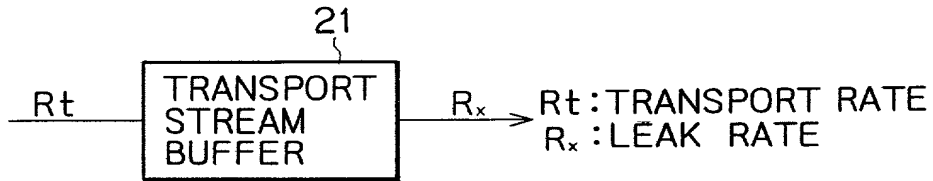


FIG. 3B

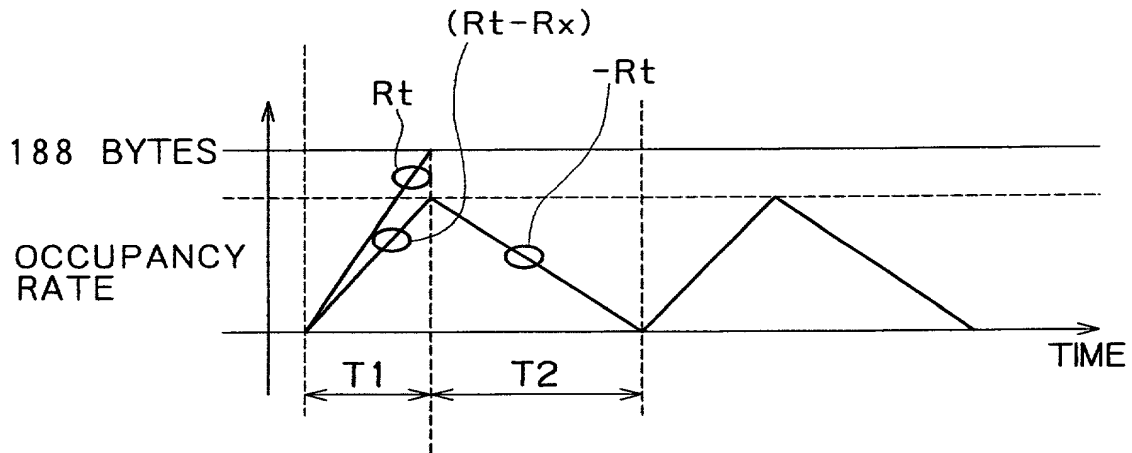


FIG. 3C

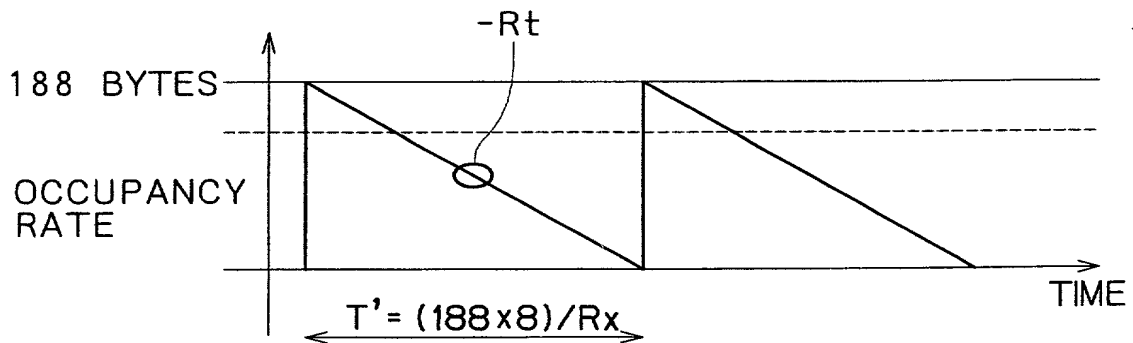


FIG. 4

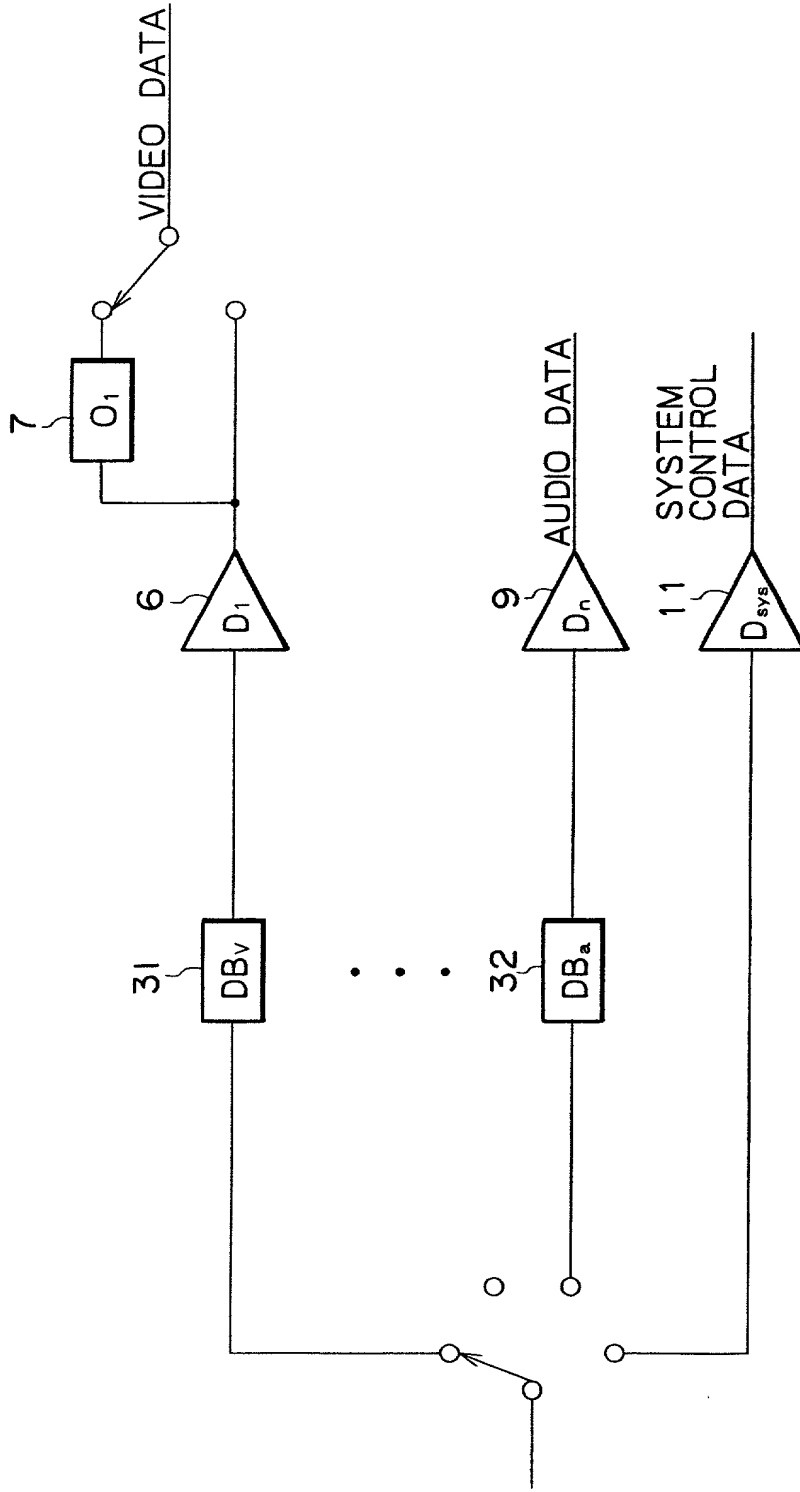


FIG. 5

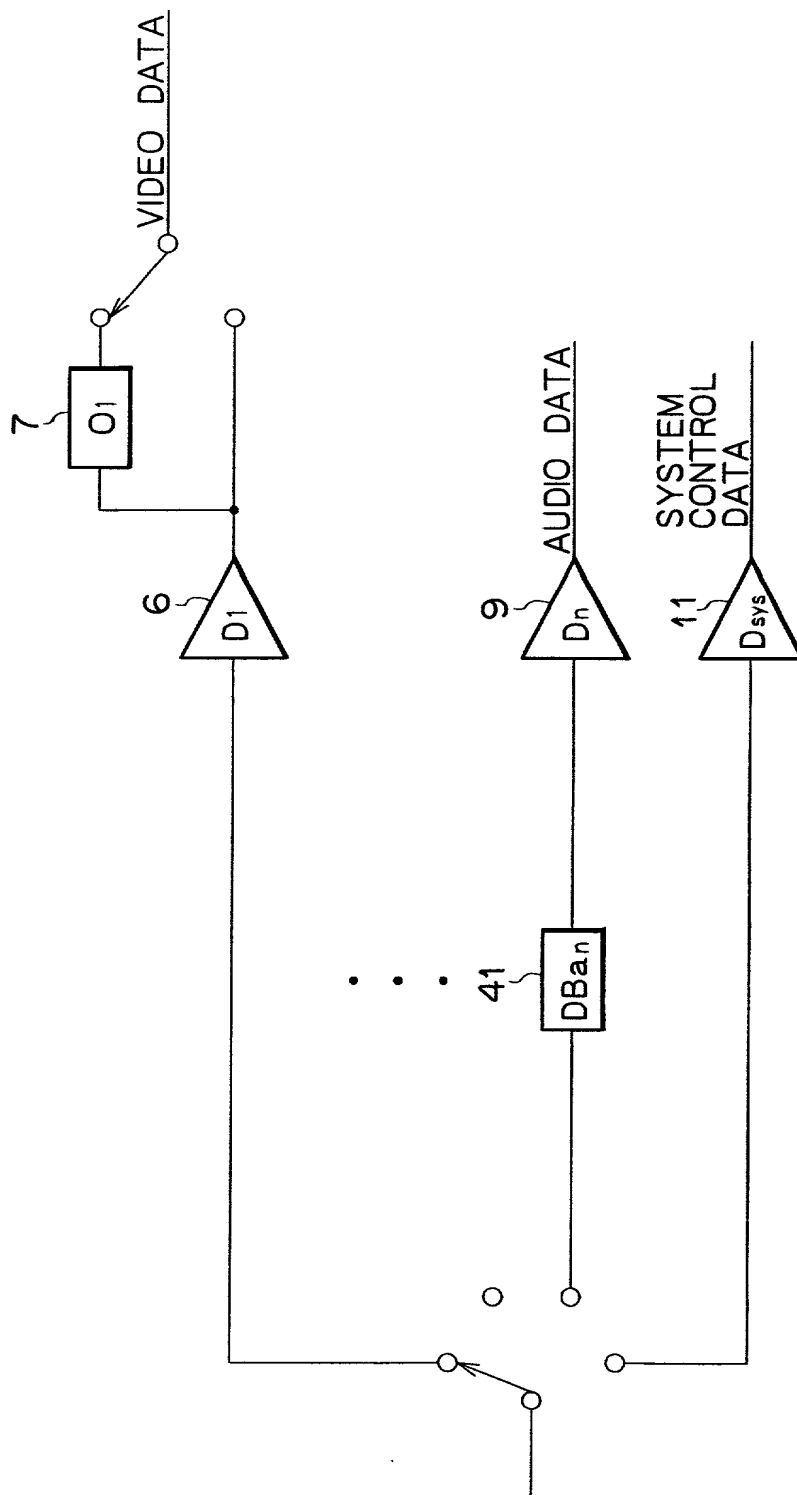


FIG. 6

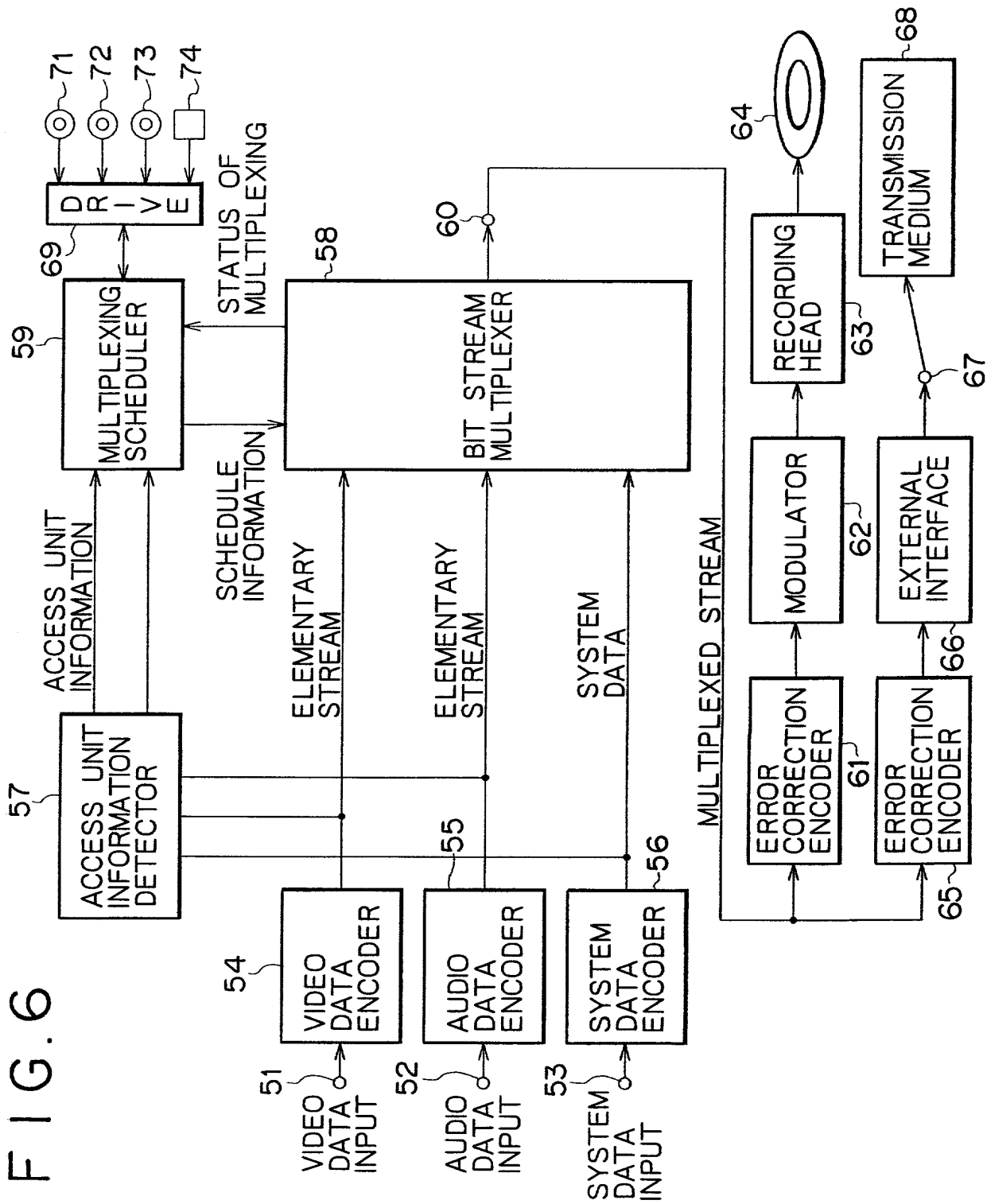
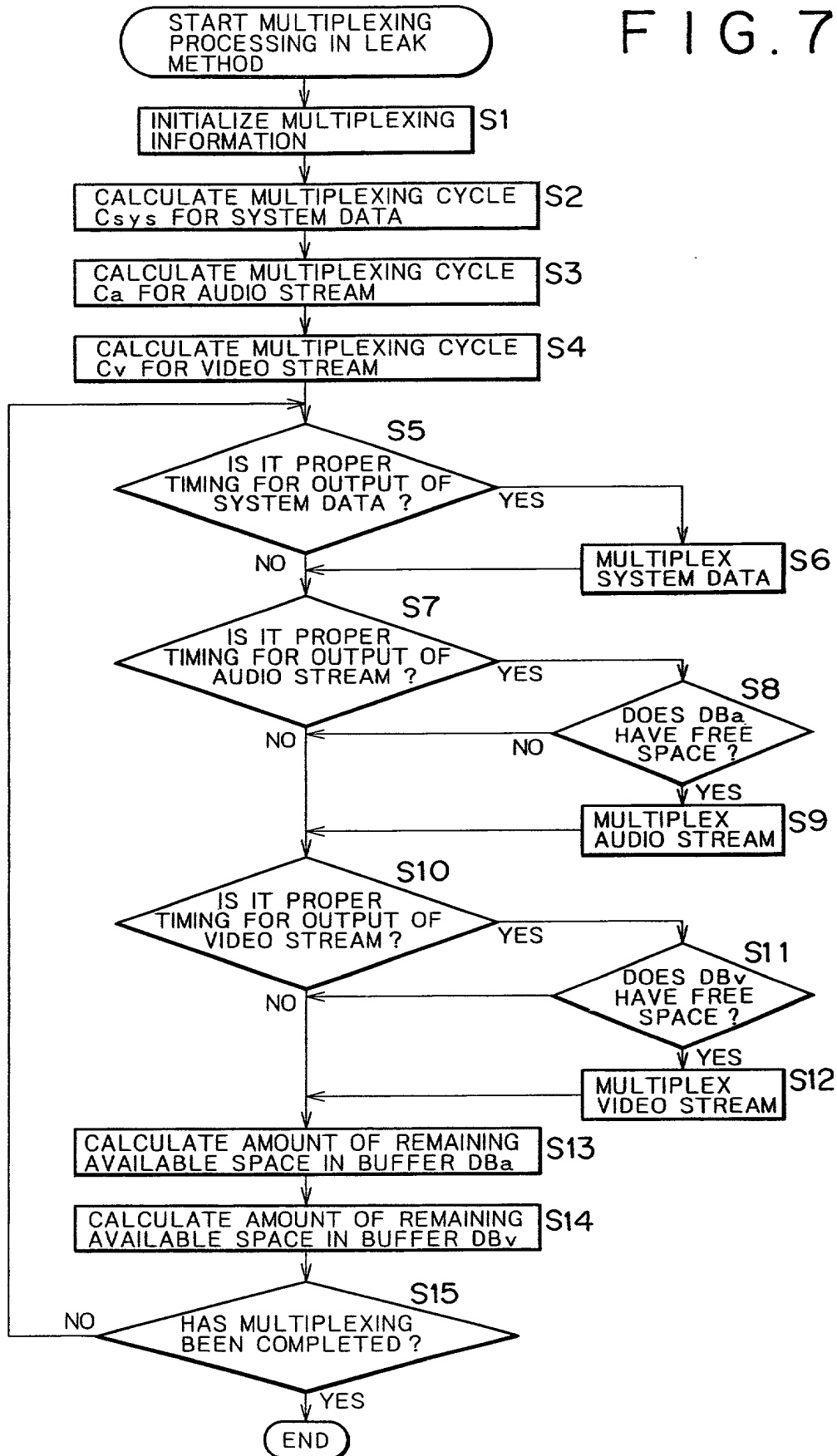


FIG. 7



START MULTIPLEXING PROCESSING
IN vbv_delay METHOD

FIG. 8

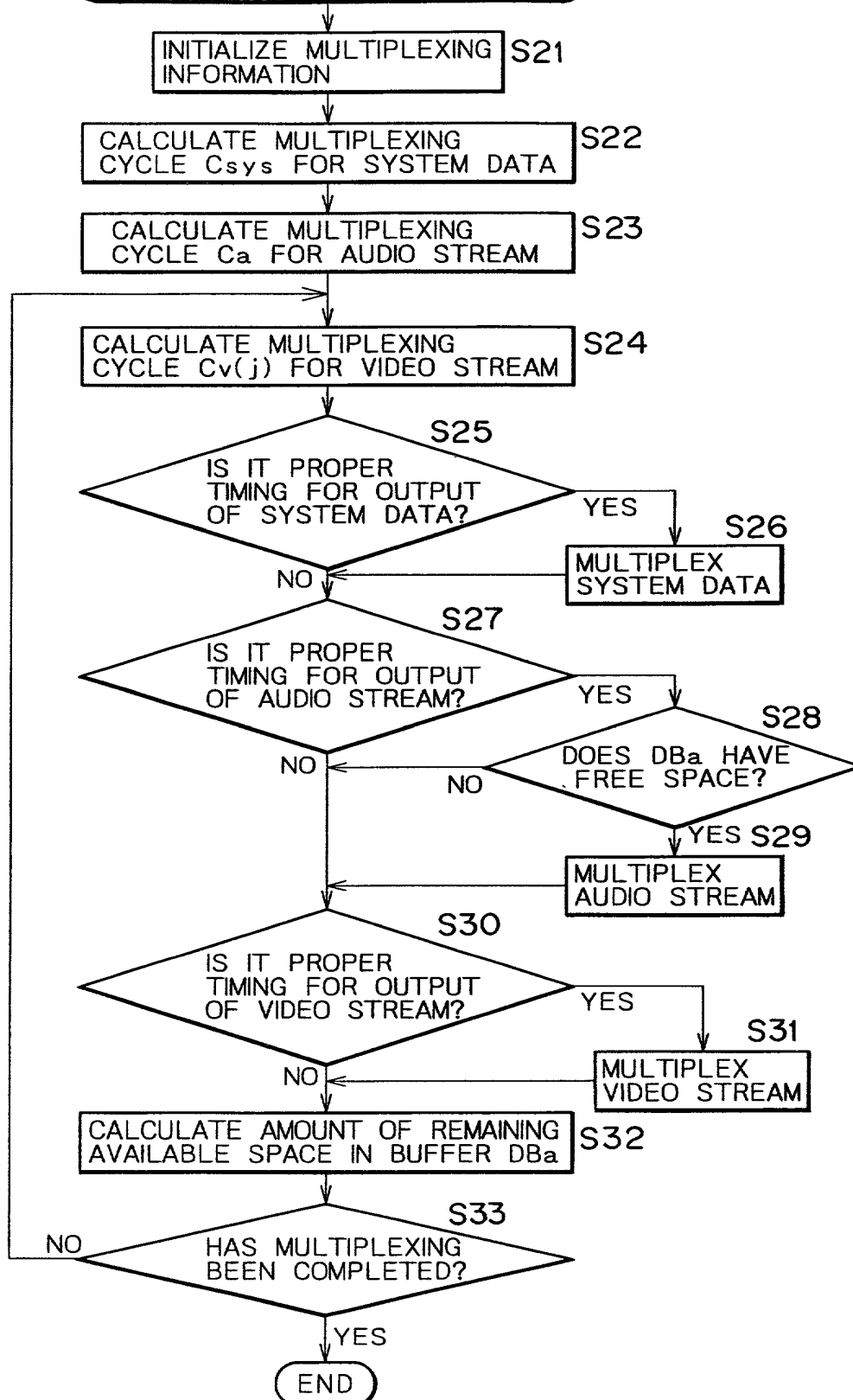
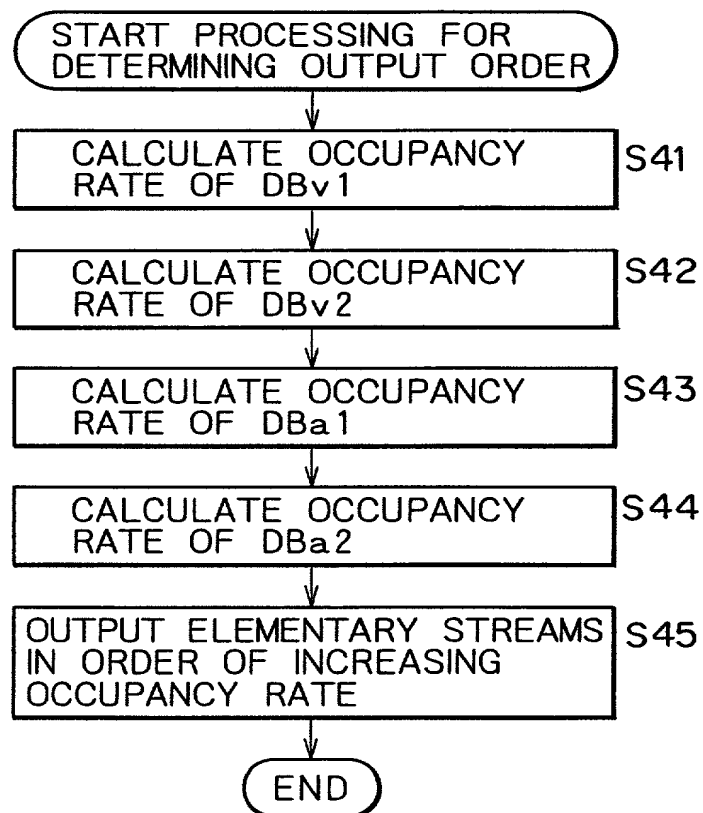


FIG. 9



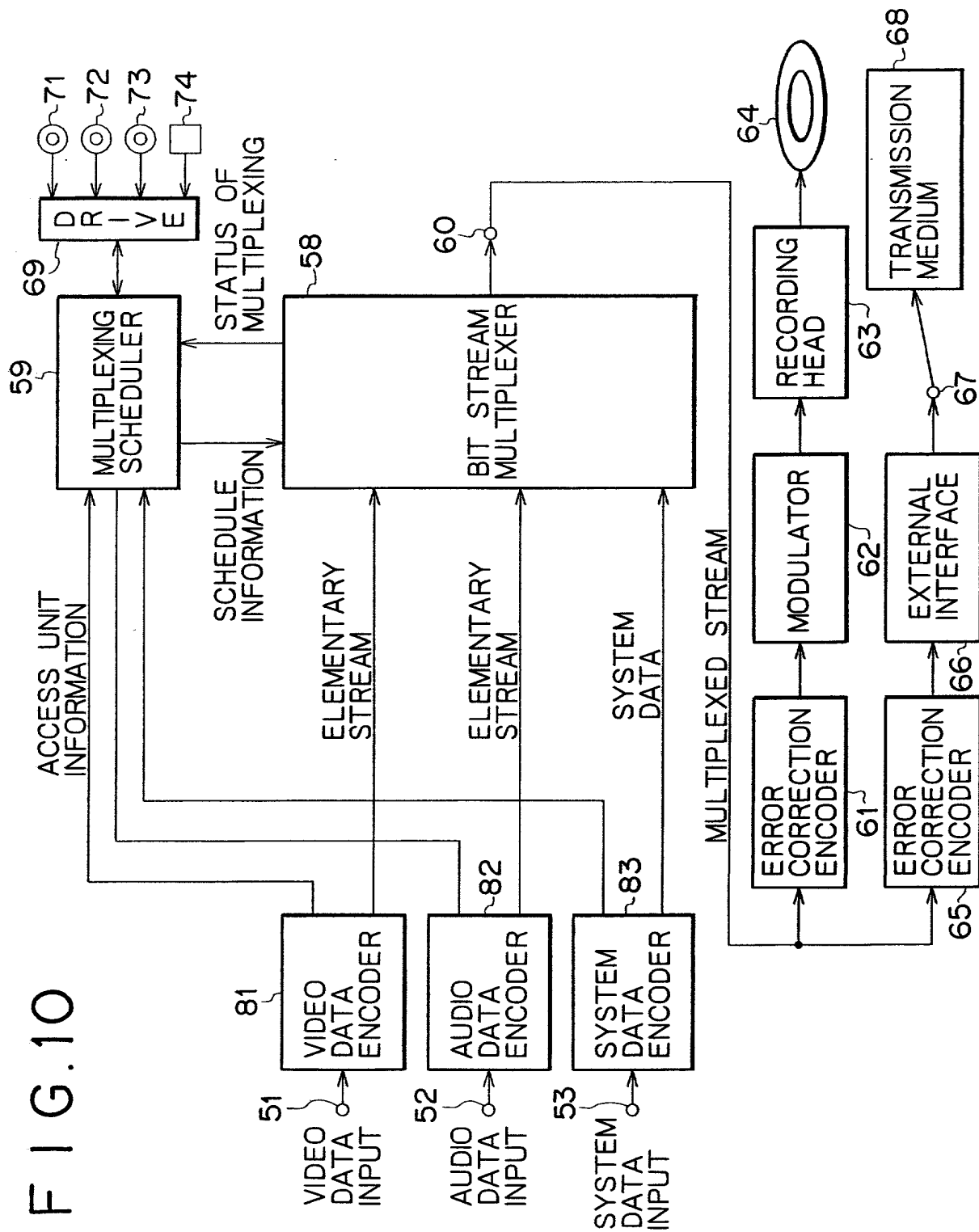
[illegible]

FIG. 11

